

Product:	XETK-S20.2D	Rev :	4	Page 1 of 9
Title :	Release-Notes			



Product :	XETK-S20.2D			
Title :	Release Notes			
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Created:	Name M. Higgins	Department ETAS-DAP/XPC-Ply	Signature M. Higgins	Date 2023-03-28
Released:	Name T. Collins	Department ETAS-DAP/XPC-Ply	Signature T. Collins	Date 2023-03-28
<b>C h a n g e s</b>				
Revision	Description	Date	Name	Signature
01	Initial Version, HSP13.1.0 and HW state C013/03	2022-5-17	M. Higgins	M. Higgins
02	Updated HDC with HSP13.2.0.	2022-09-27	M. Higgins	M. Higgins
03	Updated HDC with HSP13.3.0.	2022-12-13	M. Higgins	M. Higgins
04	Updated FW with HSP13.4.0.	2023-03-28	M. Higgins	M. Higgins

Product:	XETK-S20.2D	Rev :	4	Page 2 of 9
Title :	Release-Notes			

## Table of content

1	General Information.....	3
1.1	Safety Notice .....	3
1.2	System Requirements .....	3
1.3	Restrictions .....	3
1.4	Miscellaneous.....	4
2	Version Syntax and Tool-Chain Information .....	4
2.1	Version-Syntax of the XETK-S20.2D .....	4
2.2	Version information of the tool-chain components .....	5
2.3	Software and microcontroller support.....	5
3	What's New - Release Notes.....	6
3.1	New or Enhanced Functions .....	6
3.1.1	In HSP 13.4.0, ETK Tools 4.3.4 .....	6
3.1.2	In HSP 13.3.0 .....	6
3.1.3	In HSP 13.2.0 .....	6
3.1.4	In HSP 13.1.0 .....	6
4	Product Variants .....	7
5	Hardware Modifications.....	7
5.1	General remarks to this chapter .....	7
5.2	First delivered version .....	7
5.3	Current delivery condition .....	7
6	Firmware Modifications .....	8
6.1	General remarks to this chapter .....	8
6.2	First delivered version .....	8
6.3	Current delivery condition .....	8
7	Abbreviations .....	9

Product:	XETK-S20.2D	Rev :	4	Page 3 of 9
Title :	Release-Notes			

# 1 General Information

## 1.1 Safety Notice

Calibration activities influence the behavior of the ECU and the systems controlled by the ECU. This may result in unexpected behavior of the vehicle and thus can lead to safety critical situations. Only well-trained personnel should be allowed to perform calibration activities.

## 1.2 System Requirements

The following minimum system prerequisites must be met:

### Required Hardware

- Intel Core-i7, 4. Generation (or higher), Quad Core
- Min. 16 GB RAM
- DVD-ROM drive (for installation)
- PCIe network adapter with Intel Chipset (no USB)
- Graphics with a resolution of at least 1024x768, 256MB RAM, 16bit color and DirectX 9

### Required Operating System

- Windows® Vista SP1 (32bit) or higher,
- Windows® 7 SP1 (32 or 64bit\*) or higher.
- Windows 8 (32 / 64 bit\*)

\*) INCA uses the 32bit compatibility mode on a 64-bit operating system.

### Required Free Disk Space

- 250 MB (not including the size for application data)

The following system prerequisites are recommended:

### Recommended Hardware

- Intel Core-i7, 4. Generation, Quad Core or equivalent
- 16 GB RAM
- DVD-ROM drive (for installation)
- PCIe network adapter with Intel Chipset
- Graphics with a resolution of at least 1280 x 1024, 1GB RAM, 32bit color and DirectX 9

### Recommended Operating System

- Windows® 7 SP1 64bit (INCA uses the 32bit compatibility mode on a 64-bit operating system)

### Recommended Free Disk Space

- >500 MB

## 1.3 Restrictions

WINDOWS® 95b, WINDOWS® NT, WINDOWS® 2000, WINDOWS® 98SE, and WINDOWS® XP are not supported

Product:	XETK-S20.2D	Rev :	4	Page 4 of 9
Title :	Release-Notes			

## 1.4 Miscellaneous

To ensure the highest data throughput from the XETK device up to the PC system the following recommendations should be considered:

- Set power save mode to the highest level
- Disable virus scan
- Use network adapter for ETAS application only
- Update network adapter drivers

## 2 Version Syntax and Tool-Chain Information

### 2.1 Version-Syntax of the XETK-S20.2D

The XETK-S20.2D hardware version information is located on the product sticker and can be read out of the XETK using the firmware update tool HSP or XETK Configuration Tool.

Hardware State Syntax: **abbb/cc**

Description (modification details refer chapter 5)

<b>a</b>	PCB Version (A=V1.0, B=V1.1, C=V1.2, ...)
<b>bbb</b>	PCB Hardware State (010, 011, 012, ...)
<b>cc</b>	PCB Population Variant (00, 01, 02, ...)

The XETK-S20.2D Firmware version information can be read out of the XETK using the firmware update tool HSP or XETK Configuration Tool. It is not printed onto a XETK sticker.

Firmware-Version Syntax: **aaa.bbb.ccccc**

Description (modification details refer chapter 5)

<b>aaa</b>	Major Release (0...255)
<b>bbb</b>	Minor Release (0...255)
<b>cccc</b>	Revision/Patch (0...65535)

Firmware Packages:

HDC Work	aaa.bbb.ccccc
Firmware Work	aaa.bbb.ccccc
HDC Rescue	aaa.bbb.ccccc
Firmware Rescue	aaa.bbb.ccccc
CPLD	aaa.bbb.ccccc

Product:	XETK-S20.2D	Rev :	4	Page 5 of 9
Title :	Release-Notes			

## 2.2 Version information of the tool-chain components

To get this XETK running with the other components of the tool-chain please make sure that the version mentioned below or a newer one is used. If your software-, firmware- or hardware version is older, please update it.

If you have any problems to get this XETK running, please contact our local customer support or sales representative.

Updates or refreshes can be downloaded from the ETAS homepage:

<http://de.etasgroup.com>

<http://en.etasgroup.com>

## 2.3 Software and microcontroller support

The table reflects the initial versions which can be used with the XETK-S20.2D and the listed microcontroller.

<b>Microcontroller</b>	<b>HSP</b>	<b>INCA</b>	<b>ETK Tools</b>	<b>ASCET-RP</b>	<b>INTECRIO</b>
TC23x	V13.1.0	V7.4.1	V4.3.1	V6.4	V4.6
TC26x	V13.1.0	V7.4.1	V4.3.1	V6.4	V4.6
TC26x-ED	V13.1.0	V7.4.1	V4.3.1	V6.4	V4.6
TC27x	V13.1.0	V7.4.1	V4.3.1	V6.4	V4.6
TC27x-ED (B-Step, C-Step)	V13.1.0	V7.4.1	V4.3.1	V6.4	V4.6
TC29x	V13.1.0	V7.4.1	V4.3.1	V6.4	V4.6
TC29x-ED	V13.1.0	V7.4.1	V4.3.1	V6.4	V4.6
TC36x-PD	V13.1.0	V7.4.1	V4.3.1	V6.4	V4.6
TC37x-PD	V13.1.0	V7.4.1	V4.3.1	V6.4	V4.6
TC37x-ED	V13.1.0	V7.4.1	V4.3.1	V6.4	V4.6
TC37x-XX	V13.1.0	V7.4.1	V4.3.1	V6.4	V4.6
TC38x-PD	V13.1.0	V7.4.1	V4.3.1	V6.4	V4.6
TC39x-ED (A- Step)	V13.1.0	V7.4.1	V4.3.1	V6.4	V4.6
TC39x-ED (B- Step)	V13.1.0	V7.4.1	V4.3.1	V6.4	V4.6
TC39x-XX	V13.1.0	V7.4.1	V4.3.1	V6.4	V4.6

Product:	XETK-S20.2D	Rev :	4	Page 6 of 9
Title :	Release-Notes			

### 3 What's New - Release Notes

This chapter lists the main improvements compared to a previous shipped ETK product. Additionally, a detailed list of already known issues can be found here.

#### 3.1 New or Enhanced Functions

##### 3.1.1 In HSP 13.4.0, ETK Tools 4.3.4

Issue Identifier	Description
Feature: ETKX-2694	Add support of Monitoring Variables.

##### 3.1.2 In HSP 13.3.0

Issue Identifier	Description
Correction of TFS #699427	Updated HDC to ensure Lauterbach HW arbitration works even when the debugger uses a different IOClient

##### 3.1.3 In HSP 13.2.0

Issue Identifier	Description
Feature: ETKPRG-1224	Enable HW debugger arbitration with Lauterbach.

##### 3.1.4 In HSP 13.1.0

Issue Identifier	Description
Feature: ETKPRG-1213	Initial release of XETK-S20.2D.

Product:	XETK-S20.2D	Rev :	4	Page 7 of 9
Title :	Release-Notes			

## 4 Product Variants

The XETK-S20.2D can be purchased in only one variant. For complete details refer to the user guide.

## 5 Hardware Modifications

### 5.1 General remarks to this chapter

Hardware issues or obsolete parts can make it necessary to modify the population of the XETK. The first released version, available modifications, and current version are listed below. For the version syntax please refer to chapter 2.1.

### 5.2 First delivered version

The hardware state **C013/03** is the first delivered version.

### 5.3 Current delivery condition

The hardware state **C013/03** is delivered with all new shipments.

Product:	XETK-S20.2D	Rev :	4	Page 8 of 9
Title :	Release-Notes			

## 6 Firmware Modifications

### 6.1 General remarks to this chapter

The programmable logic code within the XETK-S20.2D is stored onto programmable logic devices (FPGA, Firmware). The first released version and current version are listed below. For the version syntax please refer to chapter 2.1.

### 6.2 First delivered version

FPGA Work	1.0.39
Firmware Work	1.0.9
FPGA Rescue	1.3.7
Firmware Rescue	1.0.9
CPLD	1.0.7

### 6.3 Current delivery condition

The following firmware versions will be programmed into all XETK-S20.2D shipments:

FPGA Work	1.3.4
Firmware Work	1.4.67
FPGA Rescue	1.3.7
Firmware Rescue	1.0.9
CPLD	1.0.7

In case of any problems, the above-mentioned components can be programmed to the XETK by using **HSP V13.4.0**. This HSP version is similar to the currently delivered XETK products. Newer HSP versions could contain bug fixes and / or new features.

**Attention:** For updating the XETK with a later version by using the HSP Firmware update tool, all XETK - packages will be updated one after another and this will last a few minutes. Update of FPGA(rescue), Firmware(rescue), and CPLD are **not** done in a failsafe manner.



Product:	XETK-S20.2D	Rev :	4	Page 9 of 9
Title :	Release-Notes			

## 7 Abbreviations

ASCET-RP	Rapid Prototyping Software of ETAS
XETK	Product (emulator test probe)
firmware	Software running on the XETK hardware; may be updated for new features or bug fixes
FPGA	<b>F</b> ield <b>P</b> rogrammable <b>G</b> ate <b>A</b> rray; interface component to the application hardware
Hot-fix	Software bug-fix for a refresh version
HSP	<b>H</b> ardware <b>S</b> ervice <b>P</b> ack; ETAS product which includes the firmware for the complete ETAS hardware, shipped together with INCA but also available as standalone product, download at ETAS homepage possible
INCA	Measurement and Calibration Software of ETAS
INTECRIO	Rapid Prototyping Software of ETAS
MC	<b>M</b> easurement & <b>C</b> alibration
PCB	<b>P</b> rinted <b>C</b> ircuit <b>B</b> oard
RP	<b>R</b> apid <b>P</b> rototyping
SBB	<b>S</b> ervice <b>B</b> ased <b>B</b> ypass
tool-chain	MC hardware (e.g. ES690) and software (e.g. INCA)
XETK Configuration Tool	Configuration Software, in order to configure a (X)ETK / FETK